

10:05 – 10:20

Introduction: Opening of the workshop

Bettina Heim (NVIDIA)

Session 1: Hardware-aware Compilation and Pulse-level Representation

10:20 – 11:00

Talks: **Jorge Echavarria** (LRZ), **Aniket Dalvi** (Duke University)

11:00 – 11:30

Panel: **Rowen Wu** (Q-CTRL), **Israel Reichental** (Classiq), **Serwan Asaad** (Quantum Machines)

Session 2: Hybrid systems and Error Correction

13:00 – 13:40

Talks: **Pete Campora** (Quantinuum) **Andrei Petrenko** (QCI)

13:40 – 14:30

Panel: **Santiago Núñez-Corrales** (University of Illinois), **Jamie Friel** (OQC), **Josh Izaac** (Xanadu), **Kenton Barnes** (Riverlane)

Session 3: HPC Integration and User Experience

15:00 – 15:40

Talks: **David Ittah** (Xanadu), **Martin Rüfenacht** (ParTec)

15:40 – 16:10

Panel: **Antonio Mezzacapo** (IBM), **Matan Vax** (Classiq), **Tuukka Hiltunen** (IQM)

16:10 – 16:30

Concluding Discussion